A Software Tool for Mapping and Executing Distributed Quantum Computations on a Network Simulator

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Abstract

The growing demand for large-scale quantum computers is motivating research on distributed quantum computing (DQC) architectures [1]. To support the research community in the design and evaluation of distributed quantum protocols, many simulators have been devised to aid with the construction of the topology and the deployment of computations on it [2, 3, 4, 5]. However, the process of setting up a simulation requires strong expertise in the simulator itself, being inconvenient for those who are only interested in quantum protocol evaluation or in the design of supporting tools such as quantum compilers. In this work, we present a software tool denoted as DQC Executor, that accepts as input the description of the network and the code of the algorithm, and then executes the simulation by automatically constructing the network topology and mapping the computation onto it, in a framework-agnostic way and transparently to the user.

DQC Executor

The tool is in its early stages and currently supports automatic deployment of distributed quantum algorithms to the NetSquid [5] simulator, which allows a detailed physical modeling of individual network components. The description of the network is provided by the user in a specific YAML format. The distributed algorithm, instead, is defined with the OpenQASM [6] language.

NetSquid is one of the most advanced platform for simulating quantum networking and modular computing systems subject to physical non-idealities. It ranges from the physical layer and its control plane up to the application level. This is achieved by integrating several key technologies: a discrete-event simulation engine, a specialized quantum computing library, a modular framework for modeling quantum hardware devices, and an asynchronous programming framework for describing quantum protocols.

Network Description with YAML

The first step in constructing a distributed quantum simulation is defining the structure and topology of the network in terms of nodes, connections, QPU's qubit map, and internal ebit coupling map. The DQC Executor tool can do this with a YAML formatted file. The choice of such format is due to its straightforward integration with NetSquid-NetConf snippet, a customizable open-source extension to NetSquid’s framework for automatic network setup.

A network such as the one illustrated in Figure 1 can be described with the YAML file shown above. Note that one must specify connections between nodes and which qubits in each node will be used as ebits. It is also possible to accurately describe the internal topology of each node, which is otherwise assumed to be all-to-all.

Distributed Algorithms with OpenQASM

Currently, DQC Executor only supports QASM files were all qubits of a QPU, including ebits, fit inside a single register. The mapping between registers and nodes, as well as qubits in a register and ebits, is derived from matching the nodes’ names and ebits numbering in the YAML file with registers’ names and ebits indexes in the QASM. Regarding the new operations needed for a distributed algorithm, they are defined as opaque gates inside the QASM itself, and then parsed by Qiskit QASM’s Parser. After the circuit has been parsed, one can visualize it and note that the opaque gates are represented by boxes, as shown in Figure 2, their implementation left to be specified later on in the simulation.

```
OPENQASM 2.0;
include "qelib1.inc";
qreg alice[3];
qreg bob[3];
qreg charlie[2];

h alice[0];
ex alice[0], alice[1];
entangle alice[0], bob[0], bob[2];
remoteCX alice[0], alice[2], bob[0], bob[2];
extangle alice[2], bob[2];
remoteCX alice[0], alice[2], bob[1], bob[2];
extangle alice[2], charlie[1];
remoteCX alice[0], alice[2], charlie[0], charlie[1];
```

Figure 2: Example of circuit creating a shared GHZ state.

Tool Structure

As depicted in Figure 3, DQC Executor is characterized by three main running phases: input parsing, pre-processing, and simulation. The first one concerns the parsing of the YAML configuration file and the OpenQASM description, to extract the required information. The pre-processing phase sets up all the components required for the simulation, verifies the coherence of the algorithm and the topology, establishes connections between nodes, and finally constructs the simulation protocols that will be executed by the simulated nodes. The final phase is indeed the execution of the computation by each node, which receives the whole quantum circuit and starts executing only the gates that pertain to it.

References


